Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	20129	pixel.clm.	US-PGPUB	OR	OFF	2005/11/12 17:47
L5	5025	(photodiode or photogate or photoelectric).clm.	US-PGPUB	OR	OFF	2005/11/12 17:47
L6	233688	(output or signal).clm.	US-PGPUB	OR	OFF	2005/11/12 17:51
L7	37538	(gate or (control$3 adj1 electrode)).clm.	US-PGPUB	OR	OFF	2005/11/12 17:51
L9	326	(reset$3 adj1 transistor).clm.	US-PGPUB	OR	OFF	2005/11/12 17:48
L10	9158	reset$3.clm.	US-PGPUB	OR	OFF	2005/11/12 17:49
L11	110	3 and 5 and 6 and 7 and 9	US-PGPUB	OR	OFF	2005/11/12 17:49
L12	165	3 and 5 and 6 and 7 and 10	US-PGPUB	OR	OFF	2005/11/12 17:49
L13	223	5 and 6 and 7 and 10	US-PGPUB	OR	OFF	2005/11/12 17:49
L14	223	5 and 6 and 7 and 10	US-PGPUB	OR	OFF	2005/11/12 17:49
L15	165	11 or 12	US-PGPUB	OR	OFF	2005/11/12 17:50
L16	223	13 and 14	US-PGPUB	OR	OFF	2005/11/12 17:50
L18	223	15 or 16	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 17:50
L19	414	(reset near3 (gate or (control$3 adj1 electrode))).clm.	US-PGPUB	OR	OFF	2005/11/12 17:51
L20	82	3 and 5 and 6 and 19	US-PGPUB	OR	OFF	2005/11/12 17:53
L21	101	5 and 6 and 19	US-PGPUB	OR	OFF	2005/11/12 17:51
L22	64	(mahito with shinohara).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 17:53

DP INVENTOR NAME SEARCH

INTERFERENCE SEARCH

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3484	reset adj1 transistor	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:47
L3	259933	imager or (imag$3 adj1 (sens$3 or array or apparatus or device or pick$1up)) or (CMOS adj1 imag$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:34
L4	2006	1 and 3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:35
L6	118	1 near1 gate near2 connect$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:44
L7	81	4 and 6	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:35
L8	0	(1 near3 control$4 near3 accordance) with ((pixel adj1 signal) or (output adj1 line))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:39
L9	0	1 with control$4 with accordance with ((pixel adj1 signal) or (output adj1 line))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:40
L10	12	1 with control$4 with ((pixel adj1 signal) or (output adj1 line))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:44
L11	16	1 with control$4 with (signal adj1 level)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:45
L13	2033	reduc$3 near3 number near2 transistors	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:43
L14	34	1 and 3 and 13	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:43
L15	1	1 with control$4 with (read$1out adj1 line)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:45

L16	636	1 near1 gate	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:44
L18	7	16 with ((pixel adj1 signal) or (output adj1 line))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:45
L19	1	16 with (signal adj1 level)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:45
L20	2	16 with (read$1out adj1 line)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:45
L21	12	1 near2 feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:47
L22	10	3 and 21	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 16:47
L23	605	348/294.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 17:11
L24	383	348/302.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 17:10
L25	735	348/308.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 17:10
L27	2796	250/208.1.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 17:11
L28	658	257/291.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 17:11
L29	768	257/292.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 17:11

L30	211	23 and CMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 17:11
L31	248	27 and CMOS and 1	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/11/12 17:12